

## **AMENDMENTS**

### **In the Claims**

1.     **(Currently Amended)** An information handling system comprising:  
a housing;  
processing components disposed in the housing and operable to generate display  
information;  
a graphics component interfaced with the processing components and operable to output  
the display information as a DVO signal;  
a ~~multiplexer~~ **selector** interfaced with the graphics component to receive the DVO signal  
and having first and second selectable outputs;  
a first TMDS transmitter interfaced with the first selectable multiplexer output and  
operable to transmit the DVO signal as a DVI output;  
a first DVI connector interfaced with the first TMDS transmitter and operable to provide  
the DVI output at the housing to an external display;  
a second TMDS transmitter interfaced with the second selectable ~~multiplexer~~ **selector**  
output and operable to transmit the DVO signal as a DVI output; and  
a docking connector interfaced with the second TMDS transmitter and operable to  
provide the DVI output at the housing to a docking station.
2.     **(Original)** The information handling system of Claim 1 further comprising:  
a docking station operable to couple to the housing and to accept the docking connector;  
and  
a second DVI connector interfaced with the docking connector and operable to provide  
the DVI output at the docking station to an external display.
3.     **(Currently Amended)** The information handling system of Claim 2 further  
comprising:  
a docking station detector operable to determine insertion of the information handling  
system into the docking station; and

a switch interfaced with the docking station detector and the ~~multiplexer~~ selector and operable to select the first TMDS transmitter if the housing is not coupled to the docking station and to select the second TMDS transmitter if the housing is coupled to the docking station.

4. (Currently Amended) The information handling system of Claim 3 wherein the ~~multiplexer~~ selector and the first and second TMDS transmitters are fabricated as an application specific integrated circuit.

5. (Original) The information handling system of Claim 3 wherein the graphics component comprises a graphics and memory controller hub.

6. (Original) The information handling system of Claim 3 wherein the graphics component comprises a graphics processor unit.

7. (Original) The information handling system of Claim 3 further comprising a projector operable to interface with the first DVI connector to present the display information when the housing is not coupled into the docking station.

8. (Original) The information handling system of Claim 3 further comprising a display monitor operable to interface with the second DVI connector to present the display information when the housing is coupled into the docking station.

9. (Original) A method for presentation of display information from an information handling system, the method comprising:

generating the display information as a DVO signal from a graphics component;  
selectively providing the DVO signal to one of a first or a second TMDS transmitter;  
transmitting display information from the first TMDS transmitter to a DVI connector coupled to a housing; and  
transmitting display information from the second TMDS transmitter to a DVI connector coupled to a docking station.

10. (Original) The method of Claim 9 further comprising interfacing the housing DVI connector to a projector.

11. (Original) The method of Claim 9 further comprising interfacing the docking station DVI connector to a display monitor.

12. (Original) The method of Claim 9 wherein selectively providing the DVO signal further comprises:

determining if the housing is coupled to the docking station;

selecting the first TMDS transmitter if the housing is not coupled to the docking station;

and

selecting the second TMDS transmitter if the housing is coupled to the docking station.

13. (Original) The method of Claim 12 wherein determining if the housing is coupled to the docking station further comprises activating a switch by the insertion or removal of the housing into the docking station.

14. **(Currently Amended)** The method of Claim 13 wherein selectively providing the DVO signal further comprises:

communicating the DVO signal to a ~~multiplexer~~ selector; and

switching the output of the DVO signal from the ~~multiplexer~~ selector to the first or second TMDS transmitter based on activation of the switch by insertion or removal of the housing into the docking station.

15. (Original) The method of Claim 14 wherein the graphics component comprises a graphics processor unit.

16. (Original) The method of Claim 14 wherein the graphics component comprises a graphics and memory controller hub.

17. **(Currently Amended)** A system for managing output of a DVI signal, the system comprising:

- a **multiplexer selector** operable to accept a DVO signal having display information;
- a first TMDS transmitter interfaced with the **multiplexer selector** and operable to output the display information to a DVI connector at an information handling system housing;
- a second TMDS transmitter interfaced with the **multiplexer selector** and operable to output the display information through a docking connector to a DVI connector at a docking station; and
- a **multiplexer selector** output selector operable to provide the DVO signal to the first TMDS selector if the information handling system is not coupled to the docking station and further operable to provide the DVO signal to the second TMDS selector if the information handling system couples to the docking station.

18. **(Currently Amended)** The system of Claim 17 wherein the **multiplexer selector**, the first TMDS transmitter and the second TMDS transmitter are integrated into an application specific integrated circuit.

19. **(Currently Amended)** The system of Claim 17 further comprising a graphics and memory controller hub interfaced with the **multiplexer selector** and operable to output the DVO signal.

20. **(Currently Amended)** The system of Claim 17 further comprising a graphics processor unit interfaced with the **multiplexer selector** and operable to output the DVO signal.

21. **(Currently Amended)** A system for managing output of a DVI signal, the system comprising:

- a TMDS transmitter operable to accept a DVO signal having display information and to output the display information to a DVI connector at an information handling system housing;
- a **multiplexer selector** interfaced with the TMDS transmitter and operable to switch the display information output by the TMDS transmitter to a DVI connector

associated with an information handling system housing or a DVI connector associated with a docking connector; and  
a ~~multiplexer~~ selector output selector operable to select the housing DVI connector if the information handling system is not coupled to the docking station and further operable to select the docking module connector if the information handling system couples to the docking station.

22. (Currently Amended) The system of Claim 21 wherein the TMDS transmitter and the ~~multiplexer~~ selector are integrated in an application specific integrated circuit.